

Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 10787239 on April 14, 2006

- 4 370/428 (0 OR, 4 XR)
Class 370 : MULTIPLEX COMMUNICATIONS
370/351 PATHFINDING OR ROUTING
370/428 .Store and forward
- 3 365/244 (0 OR, 3 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/244 MISCELLANEOUS
- 3 365/51 (2 OR, 1 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/51 FORMAT OR DISPOSITION OF ELEMENTS
- 3 370/349 (0 OR, 3 XR)
Class 370 : MULTIPLEX COMMUNICATIONS
370/310 COMMUNICATION OVER FREE SPACE
370/345 .Combining or distributing information via time
channels
370/349 ..Using messages having an address field as
header
- 3 370/401 (0 OR, 3 XR)
Class 370 : MULTIPLEX COMMUNICATIONS
370/351 PATHFINDING OR ROUTING
370/389 .Switching a message which includes an address
header
370/400 ..Having a plurality of nodes performing
distributed switching
370/401 ...Bridge or gateway between networks
- 3 455/413 (1 OR, 2 XR)
Class 455 : TELECOMMUNICATIONS
455/403 RADIOTELEPHONE SYSTEM
455/412.1 .Message storage or retrieval
455/413 ..Voice mail
- 3 709/219 (0 OR, 3 XR)
Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MULTIPLE COMPUTER OR PROCESS COORDINATING
709/217 REMOTE DATA ACCESSING
709/219 .Accessing a remote server
- 3 710/22 (2 OR, 1 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/22 .Direct Memory Accessing (DMA)
- 3 710/27 (1 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/22 .Direct Memory Accessing (DMA)
710/27 ..Via separate bus
- 3 710/7 (0 OR, 3 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING

- 710/5 .Input/Output command process
710/7 ..Concurrently performing Input/Output
 operation and other operation unrelated to Input/Output
- 2 340/7.23 (0 OR, 2 XR)
 Class 340 : COMMUNICATIONS: ELECTRICAL
 340/825 SELECTIVE
 340/825.36 .Having indication or alarm (e.g., location
 indication)
 340/7.2 ..Code responsive (i.e., paging)
 340/7.21 ...Two-way paging
 340/7.22 Acknowledgment of message receipt
 340/7.23 Including reply to query
- 2 340/7.52 (0 OR, 2 XR)
 Class 340 : COMMUNICATIONS: ELECTRICAL
 340/825 SELECTIVE
 340/825.36 .Having indication or alarm (e.g., location
 indication)
 340/7.2 ..Code responsive (i.e., paging)
 340/7.51 ...Message presentation
 340/7.52 Storing or retrieving message (e.g.,
 received message database handling)
- 2 340/7.57 (2 OR, 0 XR)
 Class 340 : COMMUNICATIONS: ELECTRICAL
 340/825 SELECTIVE
 340/825.36 .Having indication or alarm (e.g., location
 indication)
 340/7.2 ..Code responsive (i.e., paging)
 340/7.51 ...Message presentation
 340/7.57 Audible
- 2 340/825.52 (0 OR, 2 XR)
 Class 340 : COMMUNICATIONS: ELECTRICAL
 340/825 SELECTIVE
 340/825.52 .Addressing
- 2 342/357.12 (2 OR, 0 XR)
 Class 342 : COMMUNICATIONS: DIRECTIVE RADIO WAVE SYSTEMS
 AND DEVICES
 342/350 DIRECTIVE
 342/352 ..Including a satellite
 342/357.01 ...With position indicating
 342/357.06 ...Using Global Positioning Satellite (GPS or
 Glonass)
 342/357.12 GPS receiver signal processing
- 2 365/52 (1 OR, 1 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/52 HARDWARE FOR STORAGE ELEMENTS
- 2 365/63 (0 OR, 2 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/63 INTERCONNECTION ARRANGEMENTS
- 2 370/429 (0 OR, 2 XR)
 Class 370 : MULTIPLEX COMMUNICATIONS
 370/351 PATHFINDING OR ROUTING
 370/428 .Store and forward
 370/429 ..Particular storing and queuing arrangement
- 2 380/270 (1 OR, 1 XR)

10787239_CLSTITLES.txt

Class 380 : CRYPTOGRAPHY
380/255 COMMUNICATION SYSTEM USING CRYPTOGRAPHY
380/270 .Wireless communication

2 455/414.1 (0 OR, 2 XR)
Class 455 : TELECOMMUNICATIONS
455/403 RADIOTELEPHONE SYSTEM
455/414.1 .Special service

2 455/517 (0 OR, 2 XR)
Class 455 : TELECOMMUNICATIONS
455/39 TRANSMITTER AND RECEIVER AT SEPARATE STATIONS

455/500 .Plural transmitters or receivers (i.e., more
than two stations)
455/507 ..Central station (e.g., master, etc.)
455/517 ...To or from mobile station

2 709/202 (2 OR, 0 XR)
Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MULTIPLE COMPUTER OR PROCESS COORDINATING
709/201 DISTRIBUTED DATA PROCESSING
709/202 .Processing agent

2 709/203 (0 OR, 2 XR)
Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MULTIPLE COMPUTER OR PROCESS COORDINATING
709/201 DISTRIBUTED DATA PROCESSING
709/203 .Client/server

2 710/23 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/22 .Direct Memory Accessing (DMA)
710/23 ..Programmed control memory accessing

2 710/240 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/240 ACCESS ARBITRATING

2 710/45 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/36 .Input/Output access regulation
710/40 ..Access prioritization
710/45 ...Time-slot accessing

2 710/5 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/5 .Input/Output command process

2 710/52 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/52 .Input/Output data buffering

2 711/133 (0 OR, 2 XR)

10787239_CLSTITLES.txt

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Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
            SYSTEMS: MEMORY
711/100    STORAGE ACCESSING AND CONTROL
711/117    .Hierarchical memories
711/118    ..Caching
711/133    ...Entry replacement strategy

2 711/141    (2 OR, 0 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
            SYSTEMS: MEMORY
711/100    STORAGE ACCESSING AND CONTROL
711/117    .Hierarchical memories
711/118    ..Caching
711/141    ...Coherency

2 711/144    (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
            SYSTEMS: MEMORY
711/100    STORAGE ACCESSING AND CONTROL
711/117    .Hierarchical memories
711/118    ..Caching
711/141    ...Coherency
711/144    ....Cache status data bit

2 711/150    (1 OR, 1 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
            SYSTEMS: MEMORY
711/100    STORAGE ACCESSING AND CONTROL
711/147    .Shared memory area
711/150    ..Simultaneous access regulation

2 711/168    (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
            SYSTEMS: MEMORY
711/100    STORAGE ACCESSING AND CONTROL
711/167    .Access timing
711/168    ..Concurrent accessing

2 714/791    (1 OR, 1 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
            DETECTION/RECOVERY
714/699    PULSE OR DATA ERROR HANDLING
714/746    .Digital data error correction
714/786    ..Forward error correction by tree code (e.g.,
            convolutional)
714/791    ...Sequential decoder (e.g., Fano or stack
            algorithm)

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10787239_CLS.txt
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Original Classifications

2 340/7.57
2 342/357.12
2 365/51
2 709/202
2 710/22
2 711/141

Cross-Reference Classifications

4 370/428
3 365/244
3 370/349
3 370/401
3 709/219
3 710/7
2 340/7.23
2 340/7.52
2 340/825.52
2 365/63
2 370/429
2 455/413
2 455/414.1
2 455/517
2 709/203
2 710/23
2 710/240
2 710/27
2 710/45
2 710/5
2 710/52
2 711/133
2 711/144
2 711/168

Combined Classifications

4 370/428
3 365/244
3 365/51
3 370/349
3 370/401
3 455/413
3 709/219
3 710/22
3 710/27
3 710/7
2 340/7.23
2 340/7.52
2 340/7.57
2 340/825.52
2 342/357.12
2 365/52
2 365/63
2 370/429
2 380/270
2 455/414.1
2 455/517
2 709/202
2 709/203
2 710/23

10787239_CLS.txt

2 710/240
2 710/45
2 710/5
2 710/52
2 711/133
2 711/141
2 711/144
2 711/150
2 711/168
2 714/791